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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/063,086	03/18/2002	Chung-Yuan Liu	CMOP0016USA	4842
27765	7590 12/24/2003		EXAMINER	
NAIPO (NORTH AMERICA INTERNATIONAL PATENT OFFICE)			DUONG, THOI V	
	P.O. BOX 506 MERRIFIELD, VA 22116		ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 12/24/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application No.	Applicant(s)			
		10/063,086	LIU, CHUNG-YUAN			
		Examiner	Art Unit			
		Thoi V Duong	2871			
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence address			
THE - Exte after - If the - If NC - Failu - Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. The period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) day ill apply and will expire SIX (6) MONTHS from cause the application to become ARANDONE	nely filed  s will be considered timely. the mailing date of this communication.  D. (35 U.S.C. & 133)			
1)	Responsive to communication(s) filed on 05 Se	eptember 2003.				
	This action is <b>FINAL</b> . 2b) This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims					
5)□ 6)⊠ 7)□	<ul> <li>4) Claim(s) 1-3,5-9 and 11-13 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>5) Claim(s) is/are allowed.</li> <li>6) Claim(s) 1-3,5-9 and 11-13 is/are rejected.</li> <li>7) Claim(s) is/are objected to.</li> <li>8) Claim(s) are subject to restriction and/or election requirement.</li> </ul>					
	ion Papers	·				
10)	The specification is objected to by the Examiner The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the confidence Replacement drawing sheet(s) including the correction to each or declaration is objected to by the Examiner.	epted or b) objected to by the Edrawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e 37 CFR 1.85(a). lected to. See 37 CFR 1.121(d).			
	under 35 U.S.C. §§ 119 and 120	armier. Note the attached office	Action of John F 10-152.			
12)⊠ a)  * S 13)	Acknowledgment is made of a claim for foreign All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau See the attached detailed Office action for a list of acknowledgment is made of a claim for domestic ince a specific reference was included in the first 7 CFR 1.78.  1) The translation of the foreign language provinces the company of the foreign language provinces and the company of the first sentence of the efference was included in the first sentence of the efference was included in the first sentence of the efference was included in the first sentence of the	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)). of the certified copies not received priority under 35 U.S.C. § 119(extraction of the specification of the s	on No ed in this National Stage  d. e) (to a provisional application) in an Application Data Sheet. eived. and/or 121 since a specific			
Attachmen		_				
2) 🔲 Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal Pa	(PTO-413) Paper No(s) atent Application (PTO-152)			

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#### **DETAILED ACTION**

This office action is in response to the Amendment filed September 05, 2003.
 Accordingly, claims 1, 5, 7 and 13 were amended, and claims 4 and 10 were cancelled. Currently, claims 1-3, 5-9 and 11-13 are pending in this application.

### Response to Arguments

2. Applicant's arguments with respect to claims 1, 7 and 13 have been considered but are most in view of the new ground(s) of rejection.

## Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claims 1-3, 5-9, and 11-13 are rejected under 35 U.S.C. 102(e) as being anticipated by Kubota et al. (Pub. No. US 2003/0053016 A1).

With respect to claims 7-9, 11 and 12, as shown in Figs. 15-17, Kubota et al. discloses a multi-slants reflector applied in a liquid crystal display (LCD), the multi-slants reflector comprising:

- a substrate 4;
- a plurality of thin film transistors 3 disposed on the substrate;
- a reflective metal layer 2;
- a plurality of asymmetric slants 7, each comprising a multi-layered structure, located between the substrate and the reflective metal layer; and

an insulating layer 8,8' located between said reflective metal layer and said multilayered structure,

wherein each layer of said multi-layered structure has substantially different widths (Fig. 16 and page 10, paragraph 202);

wherein each of said asymmetric slants has substantially different angles between an upper surface of the reflective metal layer and an upper surface of the substrate, wherein said angles range from 6 degrees to approximately 15 degrees (page 1, paragraph 11);

wherein each of said asymmetric slants has substantially different heights (Fig. 16); and

wherein said multi-layered structure is a random composition of a gate metal layer 5', a gate insulation layer 15, an a-Si layer 16'a, an N+ layer, and a source/drain metal layer 18'a (page 8, paragraph 177).

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As shown in Fig. 18, Kubota et al. also discloses that the insulating layer 42 is made of photosensitive resin comprised of organic material (page 11, paragraphs 205 and 205).

With respect to claim 1, as shown in Figs 15-17, Kubota et al. discloses a method for manufacturing a multi-slants reflector comprising the steps of:

providing a substrate 4;

forming a plurality of thin film transistors 3 and a plurality of multi-layered structures 7 on the substrate simultaneously;

coating an organic layer on said thin film transistors and said multi-layered structures (as layer 42 in Figs. 18 and 19 and page 11, paragraphs 205 and 206);

performing a baking step to smooth the organic layer so as to form a plurality of asymmetric slants (page 11, paragraphs 205 and 206); and

forming a reflective metal layer 2 on the organic layer,

wherein each of said asymmetric slants comprises said multi-layered structure, and each layer of said multi-layered structure has substantially different widths (Fig. 16 and page 10, paragraph 202).

With respect to claim 13, as shown in Figs. 15-17, Kubota et al. discloses a method for manufacturing a multi-slants reflector comprising the steps of:

providing a substrate 4;

forming a plurality of thin film transistors 3 and a plurality of multi-layered structures 7 on the substrate simultaneously;

coating a protection layer 8,8' on said thin film transistors and said multi-layered structures; and

forming a reflective metal layer 2 on said protection layer,

wherein each layer of said multi-layered structure has substantially different widths (Fig. 16 and page 10, paragraph 202).

### Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thoi V. Duong whose telephone number is (703) 308-3171. The examiner can normally be reached on Monday-Friday from 8:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim, can be reached at (703) 305-3492.

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